TESTIMONIALS

"*Our engagement with Sivaltech has been very smooth. Sivaltech engineers were very responsible towards their assignments and effective to work with . I wish Sivaltech team good luck and more success in coming years.*"

- Sr. Manager
Fortune 500 Company

"*I have been with Sivaltech for past two years, my experience with Sivaltech has been very good. Being a female employee, I found Sivaltech environment to be safe with good work life balance. Managers are supportive and very helpful.*"

- Shilpi Gupta
Sr. Design Verification Engineer

"*Sivaltech provides great opportunity to its employees to grow and have an impact. According to me work culture, transparency and flexible approach are the biggest strengths of Sivaltech.*"

- Thejus
Lead Engineer Design Verification

SIVALTECH
Redefining Technology

@ **sales@sivaltech.com**

🌐 **www.sivaltech.com**

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SIVALTECH
Redefining Technology

ASIC/FPGA DESIGN | ANALOG MIXED SIGNAL | EMBEDDED SOFTWARE

www.sivaltech.com

ABOUT US

Sivaltech is a product engineering service company with expertise in semiconductor design and embedded software development. With our centres in U.S.A. and India, we are service partners to various fortune 500 companies in the field of Automotive, IOT, Medical electronics, Consumer electronics etc.

Sivaltech was founded with a vision of becoming a leading service provider in VLSI and Embedded Software by exceeding expectations on delivery, meeting stringent schedules, demonstrating integrity and intelligence in all our engagements.

ESSENTIALS OF CULTURE

- Ownership
- Initiatives
- Execution
- Ethics

SIVALTECH OVERVIEW

- Design Centers in U.S.A. and India
- Extensive experience in successfully delivering multisite projects
- Ability to provide custom solutions with innovative business models

WHY US ?

- Flexible Business Models
- Agile Management
- Focused Approach
- Delivering Value
- High Client Satisfaction

Employee and Customer Satisfaction — Flexibility, Integrity, Agility, Trust, Transparency, Tech Expertise, Team Work, Quality

ASIC / FPGA Design

SOC Implementation(RTL Design to GDSII)	IP / SOC Verification
RTL Design	Functional & Code Coverage
CDC / Lint / Formal	System Verilog / Verilog
Synthesis / STA / DFT	UVM / OVM

Physical Design	Post Silicon Validation
Full Chip, Low Power, High Frequency	Post-silicon and Bring-up
7nm,10nm,14nm,16nm,28nm	Validation and Debug
Timing, Reliability, Testability, DFM	Firmware

Analog Mixed Signal

Circuit Design	AMS Verification	Layout Design
High Speed IOs	Full Chip AMS Verification	Layout Development & Verif.
Power Management	AMS Model Generation	Layout Migration
Clocking Circuits	AMS Verif. Flow Development	Std Cells, RF, HiSpeed, Memory

Embedded Software

Device Software	Middleware & Applications	Testing Solutions
Device Drivers	Frameworks	Application Testing
BSP	User Interface	Field / Lab Testing
Base-Port	Multimedia Applications	

REDEFINING TECHNOLOGY